

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:	)	
	)	Examiner: Not yet assigned
Robert Chau et al.	)	
	)	Art Unit: Not yet assigned
Serial No.: Not yet assigned	)	
	)	
Filed: Herewith	)	
	)	
For: A METHOD FOR MAKING A	)	
SEMICONDUCTOR DEVICE HAVING A	)	
<u>HIGH-K GATE DIELECTRIC</u>	)	
Which is a Continuation of Application of:	)	
	)	
Serial No: 10/082,530	)	
	)	
Filed: February 22, 2002	)	
	)	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**FORMAL DRAWINGS SUBMISSION**

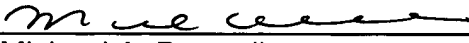
Enclosed herewith for submission in the United States Patent and Trademark Office are one (1) sheet of formal drawings for the patent application referenced above.

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02-2666. A duplicate of this sheet is enclosed for that purpose.

Respectfully submitted,

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